**CHY100 ChiPhy™ Family**

Charger Interface Physical Layer IC

**Product Highlights**

- Fully supports Quick Charge 2.0 specification
  - Class A: 5 V, 9 V, and 12 V output voltage
  - Class B: 5 V, 9 V, 12 V, and 20 V output voltage
- USB battery charging specification revision 1.2 compatible
  - Automatic USB DCP shorting D+ to D- line
  - Default 5 V mode operation
- Supports TOPSwitch and TinySwitch
- Very low power consumption
  - Below 1 mW at 5 V output
- Fail safe operation
  - Adjacent pin-to-pin short-circuit fault
  - Open circuit pin fault

**Typical Applications**

- Battery chargers for smart phones, tablets, netbooks, digital cameras, and bluetooth accessories
- USB power output ports

**Description**

CHY100 is a low-cost USB high-voltage dedicated charging port (HVDCP) interface IC for the Quick Charge 2.0 specification. It incorporates all necessary functions to add Quick Charge 2.0 capability to Power Integrations’ switcher ICs such as TOPSwitch or TinySwitch and other solutions employing traditional feedback schemes.

CHY100 supports the full output voltage range of either Class A or Class B. Optionally Class B can be inhibited for protecting the battery charger from accidental damage.

CHY100 automatically detects whether a connected Powered Device (PD) is Quick Charge 2.0 capable before enabling output voltage adjustment. If a PD not compliant to Quick Charge 2.0 is detected the CHY100 disables output voltage adjustment to ensure safe operation with legacy 5 V only USB PDs.
Pin Functional Description

**GROUND (GND) Pin**
Ground.

**V1 Pin**
Open Drain input of output voltage adjustment switch. Active for 9 V, 12 V, and 20 V output setting.

**V2 Pin**
Open Drain input of output voltage adjustment switch. Active for 12 V, and 20 V output setting.

**V3 Pin**
Open Drain input of output voltage adjustment switch. Active for 20 V output setting.

**BYPASS (BP) Pin**
Connection point for an external bypass capacitor for the internally generated supply voltage.

**REFERENCE (R) Pin**
Connected to internal band-gap reference. Provides reference current through connected resistor.

**DATA LINE D+ Pin**
USB D+ data line input.

**DATA LINE D- Pin**
USB D- data line input.
**Functional Description**

CHY100 is a low-cost USB high-voltage dedicated charging port (HVDCP) interface IC for the Quick Charge 2.0 specification. It incorporates all necessary functions to add Quick Charge 2.0 capability to Power Integrations’ integrated switcher ICs such as TOPSwitch or TinySwitch.

CHY100 also supports other solutions with traditional feedback schemes like optocoupler and secondary reference regulator TL431 as depicted in Figure 5.

**Quick Charge 2.0 Interface**

At power-up CHY100 turns on switch N5 (see Figure 3) in 20 ms or less after the BYPASS pin voltage has reached 4 V. Switch N4 and output switches N1 to N3 remain off. This sets the default 5 V output voltage level. With D+ and D- short-circuited the normal handshake between the AC-DC adapter (DCP) and powered devices (PD) as described in the USB Battery Charging Specification 1.2 can commence. After switch N5 has been turned on CHY100 starts monitoring the voltage level at D+. If it continuously stays above V_{DAT(REF)} (typ. 0.325 V) and below V_{SEL(REF)} (typ. 2 V) for at least 1.25 seconds CHY100 will enter Quick Charge 2.0 operation mode. If the voltage at D+ drops any time below 0.325 V CHY100 resets the 1.25 seconds timer and stays in USB Battery Charging Specification 1.2 compatibility mode with a default output voltage of 5 V.

Once CHY100 has entered Quick Charge 2.0 operation mode switch N5 will be turned off. Additionally switch N4 is turned on connecting a 19.53 kΩ pull-down resistor to D-. As soon as the voltage at D- has dropped low (<0.325 V) for at least 1 ms CHY100 starts accepting requests for different AC-DC adapter output voltages by means of applied voltage levels at data lines D+ and D- through the powered device. Table 1 summarizes the output voltage lookup table, corresponding AC-DC adapter output voltages and status of switches N1 to N3.

<table>
<thead>
<tr>
<th>D+</th>
<th>D-</th>
<th>Output</th>
<th>Switch Status</th>
</tr>
</thead>
<tbody>
<tr>
<td>0.6</td>
<td>0.6</td>
<td>12 V</td>
<td>N1 = N2 = On, N3 = Off</td>
</tr>
<tr>
<td>3.3</td>
<td>0.6</td>
<td>9 V</td>
<td>N1 = On, N2 = N3 = Off</td>
</tr>
<tr>
<td>3.3</td>
<td>3.3</td>
<td>20 V</td>
<td>N1 = N2 = N3 = On</td>
</tr>
<tr>
<td>0.6</td>
<td>GND</td>
<td>5 V (default)</td>
<td>N1 = N2 = N3 = Off</td>
</tr>
</tbody>
</table>

Table 1. Output Voltage Lookup Table.

For Quick Charge 2.0 Class A support only, the V3 pin has to be connected to the BYPASS pin (directly or through a resistor up to 100 kΩ). This will inhibit any requests for setting a 20 V output.

At USB cable disconnect the voltage level at D+ is pulled down by resistor R_{DAT(LKG)} (see Figure 5). Once it drops below 0.325 V CHY100 will turn on switch N5 (thereby short-circuiting D+ and D-) and turns off switches N1 to N4. This sets the default output voltage of 5 V. The recommended value for R_{DAT(LKG)} = 390 kΩ.

**Shunt Regulator**

The internal shunt regulator clamps the BYPASS pin at 6 V when current is provided through an external resistor (R_{BP} in Figure 5). This facilitates powering of CHY100 externally over the wide power supply output voltage range of 5 V to 20 V. Recommended values are R_{BP} = 4.53 kΩ and C_{BP} = 220 nF.

**BYPASS Pin Undervoltage**

The BYPASS pin undervoltage circuitry resets the CHY100 when the BYPASS pin voltage drops below 3.9 V. Once the BYPASS pin voltage drops below 3.9 V it must rise back to 4 V to enable correct operation.

**Reference Input**

Resistor R_{REF} at the REFERENCE pin is connected to an internal band gap reference and provides an accurate reference current for internal timing circuits. The recommended value is R_{REF} = 127 kΩ.

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**Figure 5. CHY100 with Traditional Output Regulation (CV Only).**

**Table 1. Output Voltage Lookup Table.**
### Absolute Maximum Ratings

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Symbol</th>
<th>Conditions</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
<th>Units</th>
</tr>
</thead>
<tbody>
<tr>
<td>BYPASS Pin Voltage</td>
<td>V&lt;sub&gt;BP&lt;/sub&gt;</td>
<td>SOURCE = 0 V; T&lt;sub&gt;j&lt;/sub&gt; = -20 °C to +85 °C (Unless Otherwise Specified)</td>
<td>4</td>
<td>5</td>
<td>6</td>
<td>V</td>
</tr>
<tr>
<td>Power-Up Reset Threshold Voltage</td>
<td>V&lt;sub&gt;BP(RESET)&lt;/sub&gt;</td>
<td>V&lt;sub&gt;BP&lt;/sub&gt; = 4.3 V, T&lt;sub&gt;j&lt;/sub&gt; = 25 °C, N1 = N2 = N3 = Off</td>
<td>2.0</td>
<td>3.9</td>
<td></td>
<td>V</td>
</tr>
<tr>
<td>BYPASS Pin Source Current</td>
<td>I&lt;sub&gt;BPSC&lt;/sub&gt;</td>
<td>I&lt;sub&gt;BP&lt;/sub&gt; = 3 mA</td>
<td>5.7</td>
<td>6</td>
<td>6.3</td>
<td>V</td>
</tr>
<tr>
<td>BYPASS Pin Shunt Voltage</td>
<td>V&lt;sub&gt;BP(SHUNT)&lt;/sub&gt;</td>
<td>V&lt;sub&gt;BP&lt;/sub&gt; = 3 mA</td>
<td>1.18</td>
<td>1.23</td>
<td>1.28</td>
<td>V</td>
</tr>
<tr>
<td>REFERENCE Pin Voltage</td>
<td>V&lt;sub&gt;R&lt;/sub&gt;</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Operating Junction Temperature</td>
<td></td>
<td>-40 °C to +150 °C</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Operating Ambient Temperature</td>
<td></td>
<td>-40 °C to 105 °C</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Storage Temperature</td>
<td></td>
<td>-65 °C to 150 °C</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Lead Temperature&lt;sup&gt;(1)&lt;/sup&gt;</td>
<td></td>
<td>260 °C</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Notes:</td>
<td></td>
<td>1. 1/16 in. from case for 5 seconds.</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

### Supply, Reference and Protection Functions

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Symbol</th>
<th>Conditions</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
<th>Units</th>
</tr>
</thead>
<tbody>
<tr>
<td>BYPASS Pin Voltage</td>
<td>V&lt;sub&gt;BP&lt;/sub&gt;</td>
<td>SOURCE = 0 V; T&lt;sub&gt;j&lt;/sub&gt; = -20 °C to +85 °C (Unless Otherwise Specified)</td>
<td>4</td>
<td>5</td>
<td>6</td>
<td>V</td>
</tr>
<tr>
<td>Power-Up Reset Threshold Voltage</td>
<td>V&lt;sub&gt;BP(RESET)&lt;/sub&gt;</td>
<td>V&lt;sub&gt;BP&lt;/sub&gt; = 4.3 V, T&lt;sub&gt;j&lt;/sub&gt; = 25 °C, N1 = N2 = N3 = Off</td>
<td>2.0</td>
<td>3.9</td>
<td></td>
<td>V</td>
</tr>
<tr>
<td>BYPASS Pin Source Current</td>
<td>I&lt;sub&gt;BPSC&lt;/sub&gt;</td>
<td>I&lt;sub&gt;BP&lt;/sub&gt; = 3 mA</td>
<td>5.7</td>
<td>6</td>
<td>6.3</td>
<td>V</td>
</tr>
<tr>
<td>BYPASS Pin Shunt Voltage</td>
<td>V&lt;sub&gt;BP(SHUNT)&lt;/sub&gt;</td>
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<td>1.18</td>
<td>1.23</td>
<td>1.28</td>
<td>V</td>
</tr>
<tr>
<td>REFERENCE Pin Voltage</td>
<td>V&lt;sub&gt;R&lt;/sub&gt;</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>V</td>
</tr>
</tbody>
</table>

### HVDCP Functions

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Symbol</th>
<th>Conditions</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
<th>Units</th>
</tr>
</thead>
<tbody>
<tr>
<td>Data Detect Voltage</td>
<td>V&lt;sub&gt;DAT(REF)&lt;/sub&gt;</td>
<td></td>
<td>0.25</td>
<td>0.325</td>
<td>0.4</td>
<td>V</td>
</tr>
<tr>
<td>Output Voltage Selection Reference</td>
<td>V&lt;sub&gt;SEL(REF)&lt;/sub&gt;</td>
<td></td>
<td>1.8</td>
<td>2</td>
<td>2.2</td>
<td>V</td>
</tr>
<tr>
<td>12 V / 20 V Output Inhibit Threshold</td>
<td>V&lt;sub&gt;INH&lt;/sub&gt;</td>
<td>V&lt;sub&gt;BP&lt;/sub&gt; - 0.6</td>
<td>10</td>
<td>20</td>
<td></td>
<td>ms</td>
</tr>
<tr>
<td>Data Lines Short-Circuit Delay</td>
<td>T&lt;sub&gt;DAT(SHORT)&lt;/sub&gt;</td>
<td>V&lt;sub&gt;OUT&lt;/sub&gt; ≥ 0.8 V See Figure 5</td>
<td></td>
<td></td>
<td></td>
<td>ms</td>
</tr>
<tr>
<td>D+ High Glitch Filter Time</td>
<td>T&lt;sub&gt;GLITCH(DC)&lt;/sub&gt;</td>
<td></td>
<td>1000</td>
<td>1250</td>
<td>1500</td>
<td>ms</td>
</tr>
<tr>
<td>Output Voltage Glitch Filter Time</td>
<td>T&lt;sub&gt;GLITCH(V)&lt;/sub&gt;</td>
<td>CHANGE</td>
<td>20</td>
<td>40</td>
<td>60</td>
<td>ms</td>
</tr>
<tr>
<td>D- Pull-Down Resistance</td>
<td>R&lt;sub&gt;DN(DWN)&lt;/sub&gt;</td>
<td></td>
<td>14.25</td>
<td>19.53</td>
<td>24.5</td>
<td>kΩ</td>
</tr>
<tr>
<td>Switch N1 On-Resistance</td>
<td>R&lt;sub&gt;DS(ON)N1&lt;/sub&gt;</td>
<td>I&lt;sub&gt;N1&lt;/sub&gt; = 200 μA</td>
<td>300</td>
<td></td>
<td></td>
<td>Ω</td>
</tr>
<tr>
<td>Switch N2 On-Resistance</td>
<td>R&lt;sub&gt;DS(ON)N2&lt;/sub&gt;</td>
<td>I&lt;sub&gt;N2&lt;/sub&gt; = 200 μA</td>
<td>300</td>
<td></td>
<td></td>
<td>Ω</td>
</tr>
<tr>
<td>Switch N3 On-Resistance</td>
<td>R&lt;sub&gt;DS(ON)N3&lt;/sub&gt;</td>
<td>I&lt;sub&gt;N3&lt;/sub&gt; = 200 μA</td>
<td>300</td>
<td></td>
<td></td>
<td>Ω</td>
</tr>
<tr>
<td>Switch N4 On-Resistance</td>
<td>R&lt;sub&gt;DS(ON)N4&lt;/sub&gt;</td>
<td>I&lt;sub&gt;N4&lt;/sub&gt; = 200 μA</td>
<td>300</td>
<td></td>
<td></td>
<td>Ω</td>
</tr>
<tr>
<td>Switch N5 On-Resistance</td>
<td>R&lt;sub&gt;DS(ON)N5&lt;/sub&gt;</td>
<td>I&lt;sub&gt;N5&lt;/sub&gt; = 200 μA, V&lt;sub&gt;(ON)&lt;/sub&gt; ≤ 3.6 V</td>
<td>20</td>
<td>40</td>
<td></td>
<td>Ω</td>
</tr>
<tr>
<td>Data Line Capacitance</td>
<td>C&lt;sub&gt;DCP(PWR)&lt;/sub&gt;</td>
<td>See Note A</td>
<td></td>
<td></td>
<td>1</td>
<td>nF</td>
</tr>
</tbody>
</table>

**NOTES:**
A. Guaranteed by design. Not tested in production.
**SO-8 (D Package)**

**Notes:**

1. JEDEC reference: MS-012.
2. Package outline exclusive of mold flash and metal burr.
3. Package outline inclusive of plating thickness.
4. Datums A and B to be determined at datum plane H.
5. Controlling dimensions are in millimeters. Inch dimensions are shown in parenthesis. Angles in degrees.
### PACKAGE MARKING

**SO-8 Package Marking**

- **A.** Power Integrations Registered Trademark
- **B.** Assembly Date Code (last two digits of year followed by 2-digit work week)
- **C.** Product Identification (Part #/Package Type)
- **D.** Lot Identification Code
### MSL Table

<table>
<thead>
<tr>
<th>Part Number</th>
<th>MSL Rating</th>
</tr>
</thead>
<tbody>
<tr>
<td>CHY100D</td>
<td>1</td>
</tr>
</tbody>
</table>

### ESD and Latch-Up Table

<table>
<thead>
<tr>
<th>Test</th>
<th>Conditions</th>
<th>Results</th>
</tr>
</thead>
<tbody>
<tr>
<td>Latch-up at 125 °C</td>
<td>JESD78D</td>
<td>&gt; ±100 mA or &gt; 1.5 V (max) on all pins</td>
</tr>
<tr>
<td>Human Body Model ESD</td>
<td>ANSI/ESDA/JEDEC JS-001-2014</td>
<td>&gt; ±2000 V on all pins</td>
</tr>
<tr>
<td>Machine Model ESD</td>
<td>JESD22-A115C</td>
<td>&gt; ±200 V on all pins</td>
</tr>
</tbody>
</table>

### Part Ordering Information

- **ChiPhy Product Family**
- **100 Series Number**
- **Package Identifier**
  - CHY 100 D
  - SO-8
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2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

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